



G1662D

High Performance Current Mode PWM Controller

1. General Description

G1662D is a highly integrated Green Mode PWM control IC, It minimizes the component counts, circuit space, and reduces the overall material cost for the power applications.

The G1662D features green-mode power-saving operation, auto gain control, and internal slope compensation, soft-start functions to optimized high performance, low standby power consumption and wide output voltage range PD adapter solutions.

At full loading, the IC operates in fixed frequency mode. When the loading goes low, it operates in Green mode with valley switching for high power conversion efficiency. When the load is very small, the IC operates in Extended Burst Mode to minimize the standby power loss. As a result, high conversion efficiency can be achieved in the whole loading range.

IC offers complete protection coverage including cycle-by-cycle current limiting(OCP), over temperature protection(OTP), output short, output and VDD over voltage protection. Excellent EMI performance is achieved with proprietary frequency shuffling technique.

Offline AC/DC flyback converter for

- PD adapters
- Wide output range adapters
- Open Frame Switching Power Supply

Features

- ◆ Built-in High-Voltage Power MOS
- ◆ Ultra low operating current at light/no load
- ◆ Adaptive loop gain compensation
- ◆ Extended burst mode control for improved efficiency and low standby power
- ◆ Valley switching operation @Green mode
- ◆ Internal OCP compensation for universal line voltage
- ◆ Power on soft start reducing MOSEFT Vds stress
- ◆ Audio noise free operation
- ◆ Protection Features
 - VDD UV lockout and Over voltage protection
 - Cycle-by-Cycle over current protection
 - Output over voltage protection
 - Secondary rectifier diode open and short circuit protection
 - Secondary winding Open and short circuit protection
 - Output short protection(SCP)
 - Over temperature protection(OTP)
 - Overload protection (OLP)
- ◆ Pb-free DFN6X5-10L

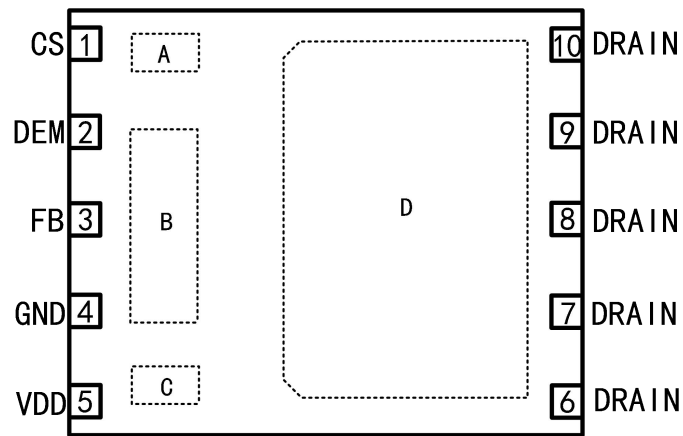


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2. Products Information

2.1 Pin configuration



DFN6X5-10L Package

Fig.1. G1662D Pin Configuration

| Pin Name | I/O | Description |
|-------------|---|--|
| GND | P | Ground. |
| FB | I | Feedback input pin. By connecting an opto-coupler to close the control loop and achieve the regulation. |
| DEM | I | Multiple functions pin. Connecting a NTC resistor to ground for OTP detection. Connecting a resistor from Vaux can adjust IOVP/ISCP trigger current and detect transformer core demagnetization. If both OTP and OVP/SCP are needed, a diode should be connected between DEM pin and the NTC resistor. |
| CS | I | Current sense input, connect it to sense the MOSFET current. |
| VDD | P | Power Supply. |
| DRAIN | O | HV MOSFET Drain Pin. The Drain pin is connected to the primary lead of the transformer |
| Exposed pad | Description | |
| A | A is internally connect to the Pin1 CS. | |
| B | B is internally connect to the Pin4 GND. | |
| C | C is internally connect to the Pin5 VDD. | |
| D | D is internally connect to the Pin6-10 DRAIN. | |